

[illegible]

0000000000 0000 0000 00000000 0000000000 0000 00000000 00000000 00
 0000000000000000 0000000 0000 00000000 0000000

[illegible][illegible][illegible][illegible]

本報告係根據本會所屬之「國家安全情報中心」所蒐集、整理、分析之資料，經本會審慎評估後，以「國家安全情報中心」之名義，向貴會提供，供貴會參考。本報告之內容，係根據本會所屬之「國家安全情報中心」所蒐集、整理、分析之資料，經本會審慎評估後，以「國家安全情報中心」之名義，向貴會提供，供貴會參考。

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The diagram consists of three rows of rectangular blocks, each block representing the number 1. The blocks are arranged in three rows and three columns. The first row has 9 blocks in the first column, 9 blocks in the second column, and 3 blocks in the third column. The second row has 7 blocks in the first column, 11 blocks in the second column, and 10 blocks in the third column. The third row has 12 blocks in the first column, 12 blocks in the second column, and 12 blocks in the third column.

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 00000000 (0000000 0 0000000) 000 000 000, 00000000 000000000000
 0000000 00000 00 0000000000, 00000 00000 000000 00000000 00000
 000 0000000 0000000 00000000 (00000) 0 000000000 0000000000000000 0000
 00000000 00000 0000000 000000, 00-0000000 0000000000000 00000000
 0000000

"[REDACTED] [REDACTED], [REDACTED] [REDACTED] [REDACTED] [REDACTED]" [REDACTED] [REDACTED] [REDACTED] [REDACTED]
[REDACTED] [REDACTED] [REDACTED] [REDACTED] [REDACTED] [REDACTED]-[REDACTED] [REDACTED] [REDACTED] [REDACTED] [REDACTED]

000000000000 00000000 0000 000000 000 0000000 000000000000
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 00000000 0000 0000000000 00000000 0000000000000000 00000000 0000
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The diagram illustrates the decomposition of a 128-bit input block into four 32-bit blocks. These blocks are then processed by a function F to produce a 128-bit output block.

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[illegible]

0000 0000 0000 000000 00000000 000000 000000 000000 000 000
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මෙම අවස්ථාවේදී පිළිගනිමින් මම මෙම, මෙමගේ මෙමගේ මෙමගේ ම
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 0000000000 0000000000000000 000000, 000000 0 000000