

The image consists of two rows of empty rectangular boxes. The top row contains 20 boxes arranged in two columns of 10. The bottom row contains 15 boxes, followed by a question mark. All boxes are empty and have a black border.

The diagram illustrates a 16-bit memory address bus. It consists of a horizontal line with 16 square boxes, each representing a bit. Above this line, there are four additional square boxes, labeled 'E1' through 'E4', positioned above the first, second, third, and fourth bit boxes respectively. This indicates that the first four bits of the address are used as enable lines, while the remaining 12 bits are used as address lines.

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Prevalence of the disease in the community

Incubation period

Latent period

Prodromal period

Period of maximum infectivity

Period of maximum severity

Convalescence

Recovery

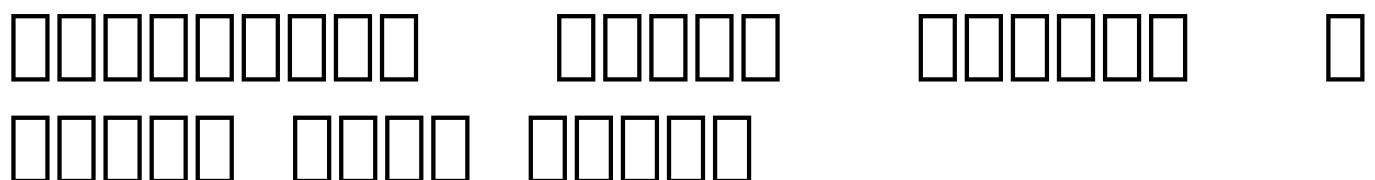
Period of immunity

Reinfection

Recovery

Period of immunity

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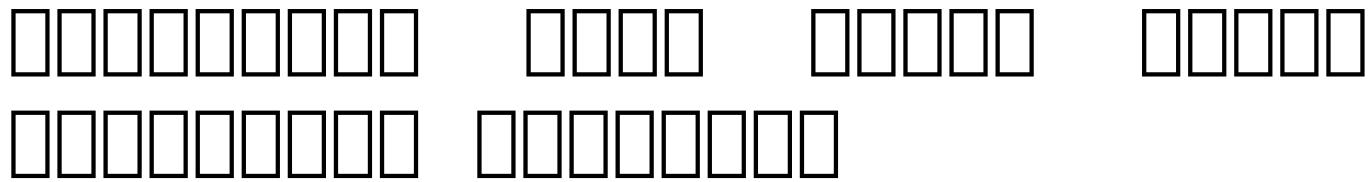


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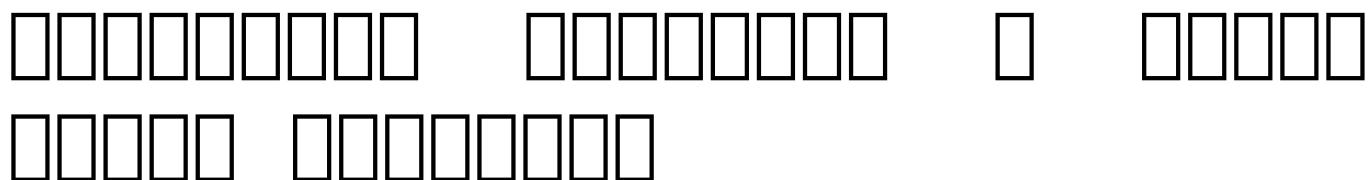
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A horizontal row of 20 empty square boxes for drawing.

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A horizontal row of nine empty rectangular boxes, each with a black border, intended for children to write their names in.

A row of seven empty rectangular boxes, each with a black border, intended for handwritten responses.

A row of seven empty rectangular boxes, each with a black border, intended for handwritten responses.

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Four empty rectangular boxes for writing, arranged horizontally.

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The image consists of two identical horizontal rows of five empty rectangular boxes. Each box is defined by a black outline and is completely white inside, intended for a person to draw or write a response. The two rows are positioned side-by-side, with a small gap between them.

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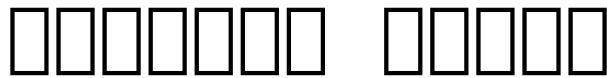
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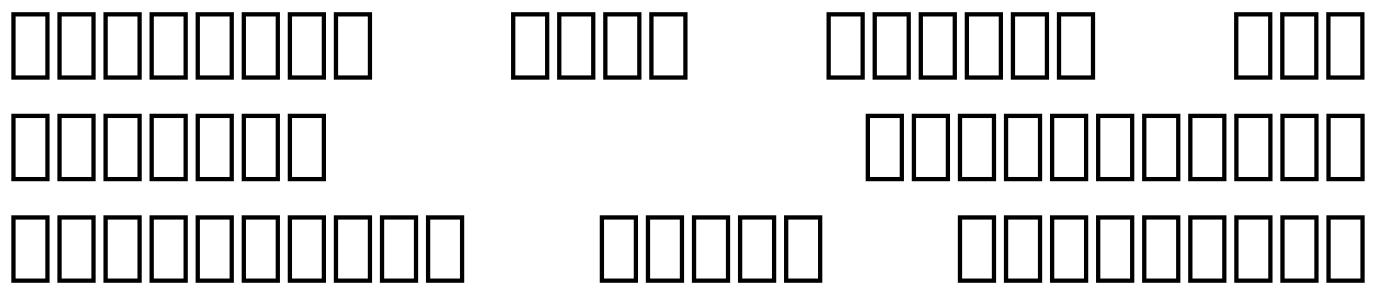
The diagram illustrates a 2D convolutional layer. The input layer at the bottom has 8 channels, each represented by a 4x4 grid of white squares. The output layer at the top has 16 channels, each represented by a 2x2 grid of blue squares. Blue lines connect the input channels to the output channels, representing active connections. The connections are organized into four main vertical columns, with each column containing four input channels and four output channels. Within each column, the connections are arranged in a 2x2 grid, corresponding to the receptive field of each output unit.

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The diagram consists of a sequence of 20 boxes arranged in three horizontal rows. The first row contains 10 white boxes. The second row contains 10 blue boxes. The third row contains 10 white boxes. This pattern of white, blue, white repeats three times across the three rows.

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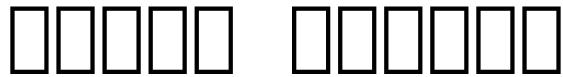


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