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《詩經》《左傳》《國語》《史記》《漢書》《後漢書》《晉書》等史籍，記載了當時的風俗習尚。

The diagram illustrates the memory map of the 6502 processor, which is organized into six horizontal rows. Each row contains eight vertical columns, representing bytes of memory. The first four rows represent the 256 bytes of RAM, while the last two rows represent the 256 bytes of ROM. The memory is divided into six 16x8 byte pages, indicated by the vertical lines separating the columns.

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